

L Number	Hits	Search Text	DB	Time stamp
1	351	257/288.ccls.	USPAT	2003/11/03 09:38
2	1172	257/347.ccls.	USPAT	2003/11/03 09:38
3	689	257/350.ccls.	USPAT	2003/11/03 09:38
6	1941	257/288.ccls. or 257/347.ccls. or 257/350.ccls.	USPAT	2003/11/03 09:43
4	307	257/365.ccls.	USPAT	2003/11/03 09:42
-	1624	((addressing or address or peripheral or periphery or logic or logical or logics or driver or drive or driving) adj (circuit or circuitry or circuits)) and (dram or (source and drain) or transistor)) and SOI	USPAT	2003/10/28 18:09
-	643	((addressing or address or peripheral or periphery or logic or logical or logics or driver or drive or driving) adj (circuit or circuitry or circuits)) and (DRAM and SOI)	USPAT	2003/10/28 18:10
-	570	((addressing or address or peripheral or periphery or logic or logical or logics or driver or drive or driving) adj (circuit or circuitry or circuits)) and (DRAM and SOI)) not (((addressing or address or peripheral or periphery or logic or logical or logics or driver or drive or driving) adj (circuit or circuitry or circuits)) and (SOI and BOX))	USPAT	2003/10/29 19:55
-	130	((addressing or address or peripheral or periphery or logic or logical or logics or driver or drive or driving) adj (circuit or circuitry or circuits)) and (SOI and BOX)	USPAT	2003/10/29 20:27
-	713	(DRAM and MOS and (circuit\$2)) and SOI	USPAT	2003/10/29 19:56
-	296	(DRAM and MOS and (circuit\$2)) and BOX	USPAT	2003/10/29 19:56
-	139	((257/\$.ccls. and DRAM) and MOS) and (source and drain)) and BOX	USPAT	2003/10/29 20:06
-	7	((257/\$.ccls. and DRAM) and MOS) and (source and drain)) and BOX) not (((DRAM and MOS and (circuit\$2)) and SOI) or ((DRAM and MOS and (circuit\$2)) and BOX))	USPAT	2003/10/29 20:27
-	254	((257/\$.ccls. and (source and drain)) and (circuit\$2)) and DRAM) and BOX	USPAT	2003/10/29 20:08
-	122	((257/\$.ccls. and (source and drain)) and (circuit\$2)) and DRAM) and BOX) not (((DRAM and MOS and (circuit\$2)) and SOI) or ((DRAM and MOS and (circuit\$2)) and BOX))	USPAT	2003/10/29 20:09
-	910	((DRAM and MOS and (circuit\$2)) and SOI) or ((DRAM and MOS and (circuit\$2)) and BOX)) not (((addressing or address or peripheral or periphery or logic or logical or logics or driver or drive or driving) adj (circuit or circuitry or circuits)) and (SOI and BOX))	USPAT	2003/11/02 23:10
-	68	((semiconductor and (first and second and third and substrate and layer)) and insulation) and film) and DRAM) and SOI) not (((DRAM and MOS and (circuit\$2)) and SOI) or ((DRAM and MOS and (circuit\$2)) and BOX)) not (((addressing or address or peripheral or periphery or logic or logical or logics or driver or drive or driving) adj (circuit or circuitry or circuits)) and (SOI and BOX)))	USPAT	2003/10/29 21:00